
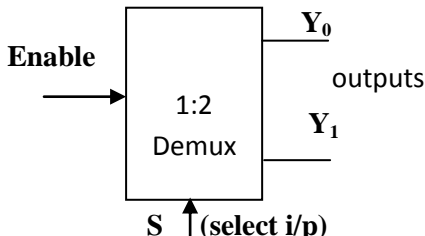





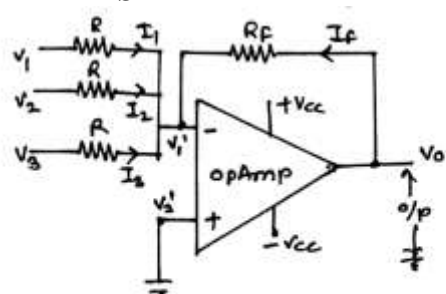
**Important Instructions to examiners:**

- 1) The answers should be examined by key words and not as word-to-word as given in the model answer scheme.
- 2) The model answer and the answer written by candidate may vary but the examiner may try to assess the understanding level of the candidate.
- 3) The language errors such as grammatical, spelling errors should not be given more Importance (Not applicable for subject English and Communication Skills).
- 4) While assessing figures, examiner may give credit for principal components indicated in the figure. The figures drawn by candidate and model answer may vary. The examiner may give credit for any equivalent figure drawn.
- 5) Credits may be given step wise for numerical problems. In some cases, the assumed constant values may vary and there may be some difference in the candidate's answers and model answer.
- 6) In case of some questions credit may be given by judgement on part of examiner of relevant answer based on candidate's understanding.
- 7) For programming language papers, credit may be given to any other program based on equivalent concept.



Q. No	Model Answer/Solution	Marks									
1	<b>Attempt any SIX of the following:</b>	12									
i	Symbol of LDR and Diode 	01 M each									
ii	<b>Thermal runaway:</b> The self-destruction of a transistor is known as thermal runaway. It is the cyclic process, which destroys the transistor. As temperature increases, transistor leakage current $I_{cbo}$ increases, which in turn increases the collector current $I_c$ due to which again junction temperature increases and so on.	02M									
iii	<b>Barkhausen's Criteria :-</b> Barkhausen's criterion is applicable for oscillators. According to this criteria the circuit has to satisfy two conditions First:- the total phase shift given by the circuit should be $360^\circ$ or $0^\circ$ . and Second :- The factor $A\beta$ should be less than equal to one.	02M									
iv	<b>Logical symbol of 1:2 De-multiplexer and its truth table</b>  <table border="1" data-bbox="869 1093 1101 1198"> <thead> <tr> <th>S</th> <th><math>Y_0</math></th> <th><math>Y_1</math></th> </tr> </thead> <tbody> <tr> <td>0</td> <td>1</td> <td>0</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> </tr> </tbody> </table>	S	$Y_0$	$Y_1$	0	1	0	1	0	1	01M 01M
S	$Y_0$	$Y_1$									
0	1	0									
1	0	1									
v	<b>Transducer :-</b> A device which converts one form of energy to other form. OR A device that converts physical energy into electrical energy. <b>Classification of Transducer:-</b> (Any one may be given marks) <b>Mechanical and Electrical</b> <b>Primary and Secondary</b> <b>Active and Passive</b> <b>Analog and Digital</b> <b>Temperature, Pressure, Humidity, Level, Thickness, Sound, Speed.....</b>	01M 01M									
vi	<b>Mechatronics:</b> - It is a multidisciplinary approach to product and manufacturing system design. Mechatronics is the synergistic integration of mechanical engineering with electronics and intelligent computer control in designing, manufacturing process and production. It helps develop atomized and efficient manufacturing/production systems to produce high quality products.	02M									
vii	<b>Symbol of BJT</b> 	01 M each									



1	viii	<p><b>Types of ADC: (Any two may be given 1 mark)</b></p> <ul style="list-style-type: none"> <li>- Flash type</li> <li>- Tracking or servo type</li> <li>- Single Slop</li> <li>- Counter Type</li> <li>- Successive Approximation</li> <li>- Dual Slope</li> </ul> <p><b>Types of DAC:</b></p> <ul style="list-style-type: none"> <li>- Binary weighted register</li> <li>- R-2R ladder</li> </ul>	01M       01M
1	B	<p><b>ATTEMPT ANY TWO</b></p> <p><b>i</b></p> <p><b>LINE AND LOAD REGULATION</b></p> <p><b>Load regulation</b> is the capability to maintain a constant output voltage despite of changes in the load current from no load (NL) to full load (FL).</p> $\% \text{ Load Regulation} = \left( \frac{V_{FL} - V_{NL}}{V_{FL}} \right) \times 100$ <p><b>Line regulation</b></p> <p>It is the change in the regulated load voltage due to change in line voltage in a specified range.</p> $\% \text{ Line regulation} = \left( \frac{V_{LH} - V_{LL}}{V_{nom}} \right) \times 100$	08M    01M    01M    01M
	ii	<p><b>OPAMP AS ADDER</b></p>  <p>Expression for output. By KCL, <math>I_1 + I_2 + I_3 + I_f = I_B</math> But <math>I_B = 0</math> <math>\therefore I_1 + I_2 + I_3 = -I_f</math> <math>I_1 = \frac{V_1}{R}, I_2 = \frac{V_2}{R}, I_3 = \frac{V_3}{R}, I_f = \frac{V_o}{R_f}</math> <math>\therefore \frac{V_1}{R} + \frac{V_2}{R} + \frac{V_3}{R} = -\frac{V_o}{R_f}</math> <math>\therefore V_o = -\frac{R_f}{R} (V_1 + V_2 + V_3)</math></p> <p style="text-align: right;"><b>Student may explain with two inputs V1 and V2 only.</b></p>	02M            02M



MAHARASHTRA STATE BOARD OF TECHNICAL EDUCATION  
(Autonomous)  
(ISO/IEC - 27001 - 2005 Certified)  
SUMMER- 17 EXAMINATION

Subject: BEM

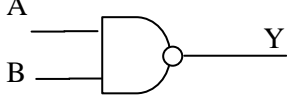
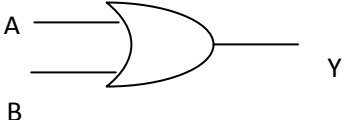
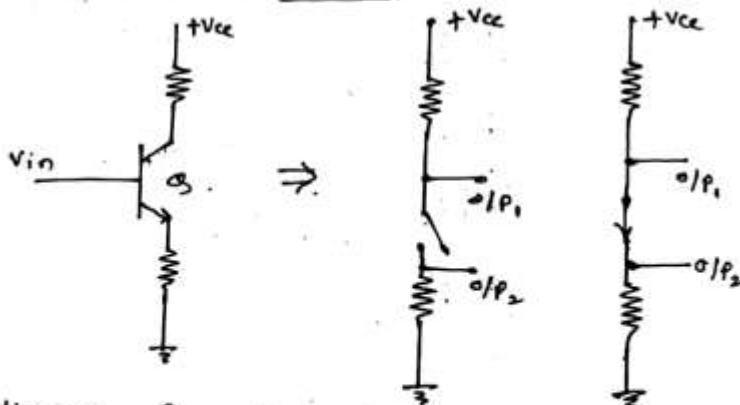
Model Answer

Subject Code:

**17302**

Q.No	Model Answer/Solution	Marks
1B	iii	
	<b>SELECTION CRITERIA FOR PLC</b> a) Type of PLC: Analog or Digital b) Number of inputs and outputs to PLC c) Operating voltage and operating current range d) Scan time of PLC e) Memory size of PLC f) Type of memory of PLC g) Type of programming h) Reliability of PLC i) Flexibility of PLC { any other relevant and appropriate criteria may also considered }	$1/2 * 8 =$ <b>04</b>
<b>2</b>	<b>ATTEMPT ANY FOUR</b>	<b>16M</b>
a	Circuit diagram of Half wave Rectifier with input and output waveforms <b>Half wave rectifier and its waveforms</b> <p>Transformer (Step-down)</p>	02M  01M  01M
b	Instrumentation Amplifier with 3opamps and its output voltage equation <p><math>V_o = (1 + 2R_1/R_g)(R_2/R)(V_2 - V_1)</math> OR <math>V_{out} = (V_2 - V_1)(1 + \frac{2R_1}{R_g})(\frac{R_2}{R})</math></p>	03M   01M

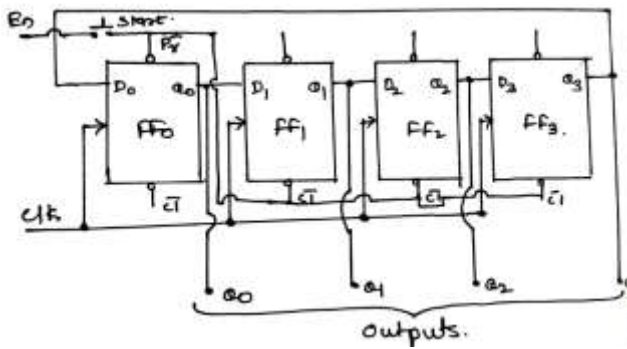
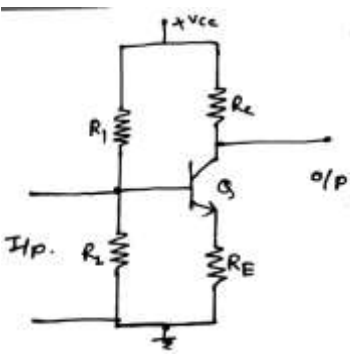


Q.No	Model Answer/Solution	Marks																														
2	<p>c</p> <p><b>Symbol of OR gate and NAND gate</b> Symbol and truth table of</p> <p><b>NAND gate</b></p>  <table border="1" data-bbox="751 577 1023 752"> <thead> <tr> <th>A</th> <th>B</th> <th>Y</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>1</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> </tr> </tbody> </table> <p><b>OR gate</b></p>  <table border="1" data-bbox="743 815 1015 990"> <thead> <tr> <th>A</th> <th>B</th> <th>Y</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> </tr> <tr> <td>1</td> <td>1</td> <td>1</td> </tr> </tbody> </table>	A	B	Y	0	0	1	0	1	1	1	0	1	1	1	0	A	B	Y	0	0	0	0	1	1	1	0	1	1	1	1	<p>02M</p> <p>02M</p>
A	B	Y																														
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1	0	1																														
1	1	1																														
	<p>d</p> <p>Trasistor as a switch</p> <p>Trasistor as a <u>switch</u>:</p>  <p><math>V_{in} = 0</math> Transistor is <u>OFF</u> switch  <math>V_{in} = +V</math> Transistor is <u>ON</u> switch          One may take o/p from <u>collector or emitter</u></p>	04M																														
	<p>e</p> <p>DAS and its Applications</p> <p><b>DAS-Data Acquisition System:-</b> The function of DAS is to collect the input data efficiently, accurately, simultaneously to store and display the data. It consists of sensor or transducer associated with signal conditioning element, multiplexer circuit, data conversion, data transmission and final storage element and display unit.</p> <p><b>Application of DAS</b>          In instrumentation system of various industries like paper, sugar, steel etc., for measurement of Temperature, pressure, velocity, thickness etc.</p>	<p>02M</p> <p>02M</p>																														



Q.No	Model Answer/Solution	Marks
2 f	<p><b>Functional block diagram of AVCS</b></p> <p>The diagram illustrates the functional blocks of an Active Vehicle Control System (AVCS). It starts with a <b>Driver</b> who provides <b>Driving Behavior</b> and <b>Environmental Information</b>. This information is processed by the <b>Identification of Driver's Characteristics</b> and <b>Reference Model with Ideal Characteristics of Vehicle Dynamics</b> (part of the <b>Driver's Characteristics Identification Subsystem</b>). The subsystem also receives <b>Vehicle States of Motion</b> feedback. The output is the <b>Selection of Reference Model</b>, which feeds into the <b>Original Reference Model</b>. The <b>Original Reference Model</b> outputs <b>Reference Outputs</b> to the <b>Integrated Controller</b>. The <b>Integrated Controller</b> also receives <b>Road Condition Information</b> and <b>Vehicle States of Motion</b> feedback. Its output goes to the <b>Tire Forces Distributor</b>, which produces <b>Horizontal, Longitudinal, Lateral Forces and Yaw Rate</b>. These forces are applied to the <b>Real Vehicle</b>, which outputs <b>Vehicle States of Motion</b> back to the driver and the subsystem.</p>	04M
3	<p><b>Attempt any Four</b></p>	16M
a	<p><b>Block diagram of Regulated power supply</b></p> <p>The block diagram shows a typical linear power supply. It starts with an AC input from the <b>To AC line</b> connected to a <b>Transformer</b>. The output of the transformer is an AC voltage <math>V_m \sin \omega t</math>. This is connected to a <b>Rectifier</b>, which produces a pulsating DC waveform. This waveform then passes through a <b>Filter</b>, which smooths the pulsations. The output of the filter is connected to a <b>Regulator</b>, which maintains a constant output voltage <math>V_{out}</math> across the <b>Load</b>. The output current is <math>I_{out}</math>. The diagram also shows the transformer's primary and secondary windings with turns <math>N</math>.</p> <p><b>Working :</b></p> <p><b>Step Down Transformer:</b> A step down transformer will step down the voltage from the ac mains to the required voltage level. The turn's ratio of the transformer is so adjusted such as to obtain the required voltage value. The output of the transformer is given as an input to the rectifier circuit.</p> <p><b>Rectification:</b> Rectifier is an electronic circuit consisting of diodes which carries out the rectification process. Rectification is the process of converting an alternating voltage or current into corresponding direct (dc) quantity. The input to a rectifier is ac whereas its output is unidirectional pulsating dc. Usually a full wave rectifier or a bridge rectifier is used to rectify both the half cycles of the ac supply (full wave rectification).</p> <p><b>Filter :</b> The rectified voltage from the rectifier is a pulsating dc voltage having very high ripple content. But this is not we want, we want a pure ripple free dc waveform. Hence a filter is used. Different types of filters are used such as capacitor filter, LC filter, Choke input filter, <math>\pi</math> type filter.</p>	02M



Q.No	Model Answer/Solution	Marks																																		
a	<p><b>Continued....</b></p> <p><b>Regulator :</b> This is the last block in a regulated DC power supply. The output voltage or current will change or fluctuate when there is change in the input from ac mains or due to change in load current at the output of the regulated power supply or due to other factors like temperature changes. This problem can be eliminated by using a regulator. A regulator will maintain the output constant even when changes at the input or any other changes occur.</p>																																			
b	<p>4 bit ring counter</p>  <table border="1" data-bbox="630 1108 1300 1433"> <thead> <tr> <th rowspan="2">Clock Count</th> <th colspan="4">Output bit Pattern</th> </tr> <tr> <th>Q0</th> <th>Q1</th> <th>Q2</th> <th>Q3</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>1</td> <td>0</td> <td>0</td> <td>0</td> </tr> <tr> <td>2</td> <td>0</td> <td>1</td> <td>0</td> <td>0</td> </tr> <tr> <td>3</td> <td>0</td> <td>0</td> <td>1</td> <td>0</td> </tr> <tr> <td>4</td> <td>0</td> <td>0</td> <td>0</td> <td>1</td> </tr> <tr> <td>5</td> <td>1</td> <td>0</td> <td>0</td> <td>0</td> </tr> </tbody> </table>	Clock Count	Output bit Pattern				Q0	Q1	Q2	Q3	1	1	0	0	0	2	0	1	0	0	3	0	0	1	0	4	0	0	0	1	5	1	0	0	0	02M  02M
Clock Count	Output bit Pattern																																			
	Q0	Q1	Q2	Q3																																
1	1	0	0	0																																
2	0	1	0	0																																
3	0	0	1	0																																
4	0	0	0	1																																
5	1	0	0	0																																
c	<p><b>Biasing:-</b> It the process of selecting the value of collector current <math>I_C</math> and Collector to emitter voltage <math>V_{ce}</math>. (selecting position of operating point of transistor on DC load line) To use transistor as an amplifier it should be always operated in active region so we have to provide proper biasing to the transistor.</p> <p><b>Voltage divider biasing for BJT</b></p>  <p><math>R_1</math> &amp; <math>R_2</math> provides voltage divider Biasing.</p>	02M  02M																																		



	Model Answer/Solution	Marks
3	<p>d <b>Signal conditioning and AC signal conditioner</b>  <b>Need of signal Conditioning:</b> It is the process used in instrumentation and measurement system to modify or improve output of the various transducers. Improvement of signal is necessary so as to make them useful and compatible with the measuring system. It consists of number of circuits or blocks.  <b>Types of circuits used in AC signal Conditioning</b></p> <ol style="list-style-type: none"> <li>Calibration and zeroing network (offset nullifying circuit)</li> <li>AC amplifier</li> <li>Phase detector</li> <li>Low pass filter</li> <li>Power supply</li> </ol>	02M  02M
	<p>e <b>Ladder diagram</b>  Ladder diagram for start stop logic with one input push button for start and one push button for stop and output motor to activate solenoid valve.</p> <p>S1 – Stop button  S2 – Start button  M- motor  V- Solenoid valve</p> <p>Student may draw different ladder diagram as per his logic, if logic is correct it may also be considered</p>	04M
	<p>F <b>Pin diagram of IC 741 Op-amp</b></p> <p>1 and 5 Offset null, to make output offset voltage to zero. 10k pot is connected between</p>	02M

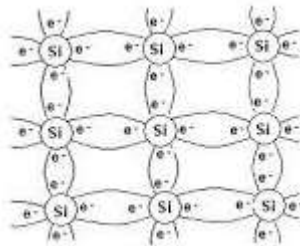




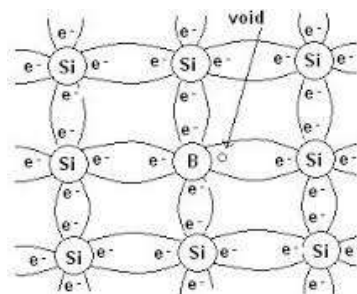
- pin 1 and pin 5 and variable terminal is connected to -Vee supply.
- 2 Inverting input, input applied to this terminal is  $180^\circ$  phase shifted at output or inverted
- 3 Non inverting input, input applied to this terminal is in phase (no phase shift)
- 4 -Vee, negative voltage is applied to this pin
- 6 Output, output of IC 741 is available at this pin which is measured w.r.t. ground
- 7+Vcc, positive voltage is applied to this pin
- 8 Not Connected

4  
a.

**Intrinsic semiconductor :-** These is a pure form of semiconductors available in nature. It has high resistivity silicon and germanium is the most popularly used semiconductors. Each atom of semiconductor forms a covalent bond with neighbouring atom and completes its octet. **Fig. silicon structure**



**Extrinsic semiconductor :-** when some impurity is added to a pure semiconductor, the resultant semiconductor is known as Extrinsic semiconductor. The process of adding impurity is called as Doping. Generally trivalent and pentavalent impurities are used to get p-type and n-type semiconductors respectively. **Fig. p type semiconductor**



2Marks

2 Marks

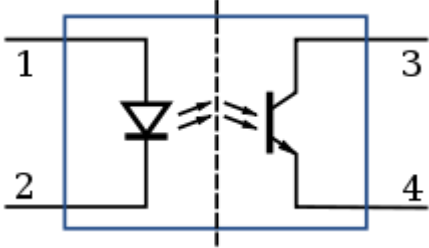
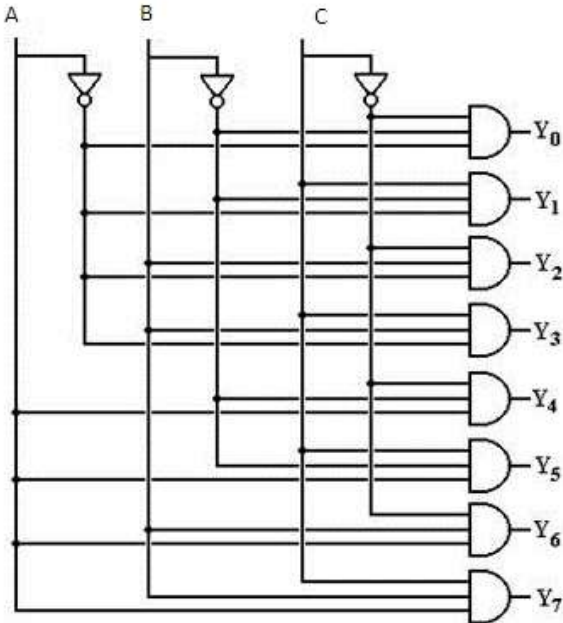


Q.No	Model Answer/Solution	Marks																				
4	<p>b</p> <p>Two stage RC coupled amplifier and its frequency response</p> <p>Frequency Response Curve of a RC Coupled Amplifier</p>	<p>02M Circuit diagram</p> <p>02M Frequency response</p>																				
	<p>c</p> <p><b>Real time Mechatronics systems and its advantages and disadvantages</b> A mechatronic system which performs various functions or operations with respect to time is referred as real time Mechatronics system. e.g. PLC,SCADA,HMI etc...</p> <p><b>Advantages:</b> -</p> <ol style="list-style-type: none"> <li>1. Fast speed of response</li> <li>2. High accuracy</li> <li>3. More flexible</li> <li>4. Overall cost is low</li> </ol> <p><b>Disadvantages:-</b></p> <ol style="list-style-type: none"> <li>1. More complex</li> <li>2. Need of expert</li> <li>3. Difficult to maintain</li> <li>4. Due to mechanical parts more Noisy system</li> </ol> <p>(Any suitable and relevant point may also be considered above)</p>	<p>02M</p> <p>01M (Any 2)</p> <p>01M (Any 2)</p>																				
	<p>d</p> <p><b>Half adder circuit</b> <b>Half Adder</b> – A combinational circuit used to perform addition of two binary bits and produces Sum and Carry bits as a result. <b>Logical Circuit diagram-</b></p> <table border="1"> <thead> <tr> <th>A</th> <th>B</th> <th>Sum</th> <th>Carry</th> </tr> </thead> <tbody> <tr> <td>0</td> <td>0</td> <td>0</td> <td>0</td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>0</td> </tr> <tr> <td>1</td> <td>0</td> <td>1</td> <td>0</td> </tr> <tr> <td>1</td> <td>1</td> <td>0</td> <td>1</td> </tr> </tbody> </table>	A	B	Sum	Carry	0	0	0	0	0	1	1	0	1	0	1	0	1	1	0	1	<p>01 M</p> <p>02M</p> <p>01 M.</p>
A	B	Sum	Carry																			
0	0	0	0																			
0	1	1	0																			
1	0	1	0																			
1	1	0	1																			



Q.No	Model Answer/Solution	Marks																								
4	<p>e</p> <p><b>Oscillator</b>:- A circuit which produces continuous oscillations at output is known as Oscillator. <b>Positive Feedback</b> is required for oscillators</p> <p>Types</p> <pre> graph TD     Types --&gt; Sinusoidal     Types --&gt; NonSinusoidal[Non sinusoidal(multivibrators)]     Sinusoidal --&gt; LowFrequency[Low frequency]     Sinusoidal --&gt; HighFrequency[High frequency]     LowFrequency --&gt; WeinBridge[Wein bridge]     LowFrequency --&gt; RCPhaseShift[RC phase shift]     HighFrequency --&gt; Colpitts[Colpitt's oscillator]     HighFrequency --&gt; Hartley[Hartley oscillator]     HighFrequency --&gt; Crystal[Crystal oscillator]           </pre>	<p>01M</p> <p>01M</p> <p>02M (Types)</p>																								
f	<p>For 4:1 multiplexer , it requires 2 select inputs.</p> <p>INPUTs</p> <p>Strobe input</p> <p>Select inputs</p> <p>OUTPUT</p> <p>Truth table</p> <table border="1"> <thead> <tr> <th>G<sub>bar</sub></th> <th>S<sub>2</sub></th> <th>S<sub>1</sub></th> <th>Y</th> </tr> </thead> <tbody> <tr> <td>1</td> <td>X</td> <td>X</td> <td>0</td> </tr> <tr> <td>0</td> <td>0</td> <td>0</td> <td>I<sub>0</sub></td> </tr> <tr> <td>0</td> <td>0</td> <td>1</td> <td>I<sub>1</sub></td> </tr> <tr> <td>0</td> <td>1</td> <td>0</td> <td>I<sub>2</sub></td> </tr> <tr> <td>0</td> <td>1</td> <td>1</td> <td>I<sub>3</sub></td> </tr> </tbody> </table>	G <sub>bar</sub>	S <sub>2</sub>	S <sub>1</sub>	Y	1	X	X	0	0	0	0	I <sub>0</sub>	0	0	1	I <sub>1</sub>	0	1	0	I <sub>2</sub>	0	1	1	I <sub>3</sub>	<p>02M</p> <p>02M</p>
G <sub>bar</sub>	S <sub>2</sub>	S <sub>1</sub>	Y																							
1	X	X	0																							
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0	1	1	I <sub>3</sub>																							



Q.No	Model Answer/Solution	Marks
5	<p>a</p> <p><b>Opto coupler:</b> a device containing light-emitting and light-sensitive components, used to couple isolated circuits.</p>  <p><b>Isolation :</b> opto-isolator contains a source (emitter) of light, almost always a near infrared light-emitting diode (LED), that converts electrical input signal into light. Intensity of light is proportional to current flowing through LED. A photosensor, which detects incoming light and either generates electric energy directly, or modulates electric current flowing from an external power supply. The sensor can be a photoresistor, a photodiode, a phototransistor. In this way opt coupler provides electrical isolation and couple the input signal to the output without any physical contact.</p>	01  03
	<p>b</p> <p><b>Decoder :</b> It is a combination logic circuit used to select a device or decode. Decoders consist of number of inputs (N) and number of output(M) and both are related as <math>2^N \geq M</math>. The standard decoders are 1:2,2:4,3:8 and so on.....</p>  <p style="text-align: center;"><b>Logic Diagram</b></p>	01  02



**Truth Table**

Inputs				Outputs							
EN	A	B	C	Y <sub>7</sub>	Y <sub>6</sub>	Y <sub>5</sub>	Y <sub>4</sub>	Y <sub>3</sub>	Y <sub>2</sub>	Y <sub>1</sub>	Y <sub>0</sub>
0	x	x	x	0	0	0	0	0	0	0	0
1	0	0	0	0	0	0	0	0	0	0	1
1	0	0	1	0	0	0	0	0	0	1	0
1	0	1	0	0	0	0	0	0	1	0	0
1	0	1	1	0	0	0	0	1	0	0	0
1	1	0	0	0	0	0	1	0	0	0	0
1	1	0	1	0	0	1	0	0	0	0	0
1	1	1	0	0	1	0	0	0	0	0	0
1	1	1	1	1	0	0	0	0	0	0	0

01

c

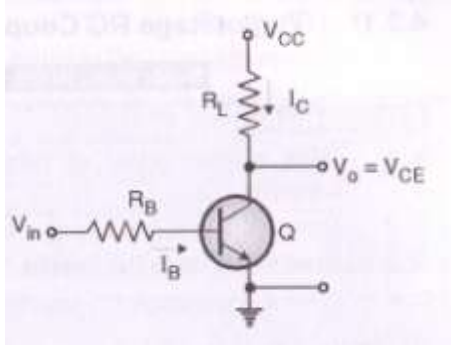
Explanation :

Transistor as current amplifier:

Current gain of CE configuration is given as  $I_C/I_B = \beta$

As the value of  $\beta$  is much higher than 1, we can say that the current gain is large and transistor acts as current amplifier.

Transistor as a voltage amplifier:



From fig. we can say that, small change in input there is small change in  $I_B$

Hence,  $\Delta I_B = \Delta V_{in}/R_B$  ----- 1

Therefore corresponding change in collector current is given by,

$\Delta I_C = \beta \Delta I_B$  ..... 2

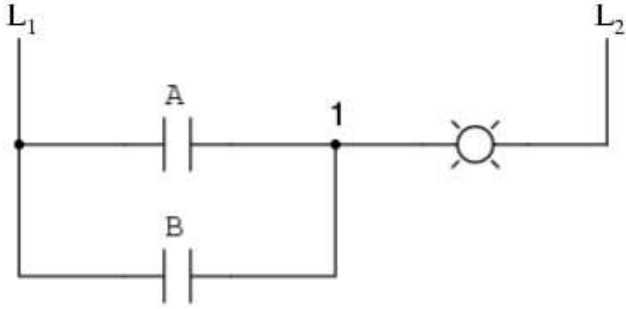
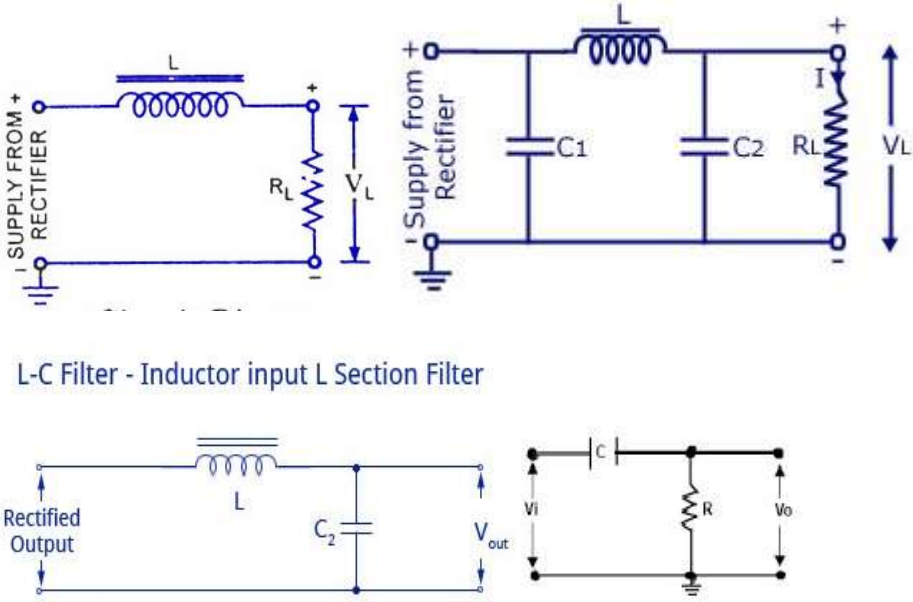
Therefore corresponding change in output voltage is given by,

02

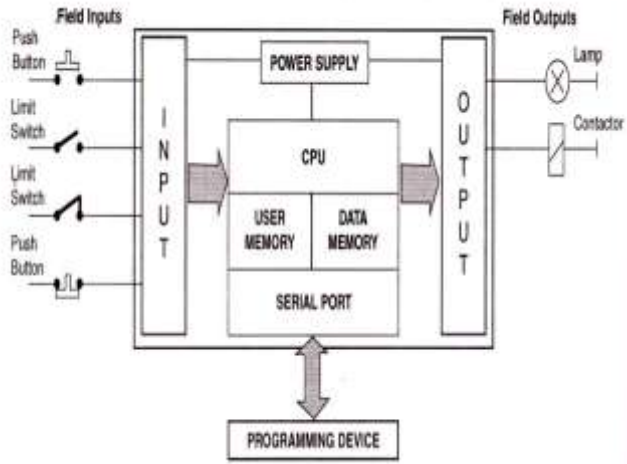
02





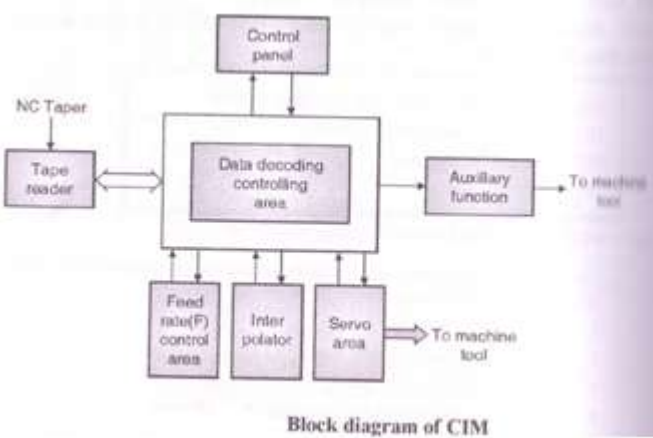
f	<p>Ladder diagrams are specialized schematics commonly used to document industrial control logic systems. They are called "ladder" diagrams because they resemble a ladder, with two vertical rails (supply power) and as many "rungs" (horizontal lines) as there are control circuits to represent.</p>  <p><b>Explanation :</b> The lamp will come on if either contact A or contact B is actuated, because all it takes for the lamp to be energized is to have at least one path for current from wire L1 to wire L2. It is a simple OR logic function, implemented with nothing more than contacts and a lamp. ( Student may draw different ladder diagram as per their logic, if logic is correct it may also be considered )</p>	04
6 a	<p><b>Filter:</b> A filter is electronic circuit which is used to remove ripple contents ( ac variations) from the rectified voltage.</p> <p><b>Types of filter</b></p> <ul style="list-style-type: none"> <li>- Series Inductor filter ( L )</li> <li>- Parallel capacitor filter ( C )</li> <li>- LC filter</li> <li>- C-L-C filter ( <math>\pi</math> )</li> </ul>  <p>Any one diagram can be considered</p>	01 02 01



b	<p>“Programmable logic controller is defined as a sequential logic device that generates output signals” according to the logic operations performed on the input signals.” or PLC is a digitally operated electronic system which used programmable memory for the internal storage of user-oriented instructions for implementing specific functions such as logic sequencing, timing counting and arithmetic to control through analog inputs and outputs, various types of machines or processes</p> 	02  02															
c.	<table border="1"> <thead> <tr> <th>Parameter</th> <th>RC oscillator</th> <th>LC Oscillator</th> </tr> </thead> <tbody> <tr> <td>component used</td> <td>Resistor (R) and Capacitor (C)</td> <td>Inductor (L) and Capacitor (C)</td> </tr> <tr> <td>frequency range</td> <td>Low and medium</td> <td>high</td> </tr> <tr> <td>frequency stability</td> <td>poor</td> <td>poor</td> </tr> <tr> <td>application</td> <td>low and medium frequency signal generator</td> <td>in radio, TV, frequency synthesizers</td> </tr> </tbody> </table>	Parameter	RC oscillator	LC Oscillator	component used	Resistor (R) and Capacitor (C)	Inductor (L) and Capacitor (C)	frequency range	Low and medium	high	frequency stability	poor	poor	application	low and medium frequency signal generator	in radio, TV, frequency synthesizers	01 mark to each correct point
Parameter	RC oscillator	LC Oscillator															
component used	Resistor (R) and Capacitor (C)	Inductor (L) and Capacitor (C)															
frequency range	Low and medium	high															
frequency stability	poor	poor															
application	low and medium frequency signal generator	in radio, TV, frequency synthesizers															
d	<p><b>Data Logger</b> : A data logger, ” is an electronic instrument that records measurements of the instruments located at different parts of plant at set intervals over a period of time. Data logger measure and record data effortlessly as quickly, as often and as accurately desired.</p> <p><b>Applications: (any four)</b></p> <ul style="list-style-type: none"> <li>- power plant</li> <li>- Petrochemical plant</li> <li>- cement plant</li> <li>- AVCS</li> <li>- fertilizer industries</li> <li>- oil refinery</li> <li>- engine testing</li> </ul>	02  ½ mark each															
e	<ul style="list-style-type: none"> <li>- It is an 8 bit microprocessor</li> <li>- It is manufactured with N-MOS technology implemented with 6200 transistors.</li> <li>- It has 16-bit address lines - A0-A15</li> <li>- The first 8 lines of address bus and 8 lines of data bus are multiplexed AD0-AD7. Data bus is</li> </ul>	½ mark each to any correct 8															





	<p>a group of 8 lines D0-D7.</p> <ul style="list-style-type: none"><li>- It provides 5 level interrupts and supports external interrupt request.</li><li>- A 16 bit program counters (PC).</li><li>- A 16 bit stack pointer (SP).</li><li>- It consists of 74 instruction sets.</li><li>- It requires a signal +5V power supply</li></ul> <p>- operates at 3.2 MHZ single phase clock with maximum clock frequency 6 MHz and minimum clock frequency 500 kHz.</p> <p>- It provides 1 accumulator, 2 flag register, six 8-bit general purpose register arranged in pairs: BC, DE, HL and 2special purpose registers.</p> <p>- It performs arithmetic and logical operations.</p> <p>- 8085 microprocessor requires two phase, 50% duty cycle, TTL clock. These clock signals are generated by an internal clock generator</p> <p>- It generates 8 bit I/O address, hence it can access <math>2^8 = 256</math> input ports and 256 output ports.</p> <p>- 8085 microprocessor can be used to implement three chip microcomputer (8085, 8155, 8355)</p>	<p>points</p>
<p>f</p>	<p>Computer-integrated manufacturing (CIM) is the manufacturing approach of using computers to control the entire production process. This integration allows individual processes to exchange information with each other and initiate actions. It consist of 4 basic components :</p> <ol style="list-style-type: none"><li>1) Machine tool &amp; related equipment</li><li>2) Material handling system</li><li>3) Computer system</li><li>4) Human labour</li></ol>  <p>Block diagram of CIM</p>	<p>02</p> <p>02</p>